



Call for Papers, Workshops, Tutorials, Special Sessions

- CASES • CODES+ISSS • EMSOFT • MEMOCODE • NOCS • Workshops • Tutorials •

About Embedded Systems Week (ESWEEK)

Embedded Systems Week (ESWEEK) is the premier event covering all aspects of hardware and software design for smart, intelligent, and connected computing systems. By bringing together three leading conferences (ACM/IEEE CASES, CODES+ISSS, EMSOFT), one symposium (MEMOCODE), and several workshops and tutorials, ESWEEK allows attendees to benefit from a wide range of topics covering the state of the art in embedded systems research and development.

One Registration, Three Conferences!

Registered attendees can attend sessions in any of the ESWEEK conferences (ACM/IEEE CASES, CODES+ISSS, EMSOFT). Please note that tutorials, symposium (MEMOCODE), and workshops may use separate registration.

Journal Track:

Abstract Submission: March 23, 2025
 Full Paper Submission: March 30, 2025 (firm)
 First Round Notification: May 18, 2025
 Revised Paper Submission: June 15, 2025
 Notification of Acceptance: July 13, 2025

Late Breaking Track:

Paper Submission: June 1, 2025 (firm)
 Notification of Acceptance: July 13, 2025

Proposals of Workshops, Tutorials, Education Classes, and Special Sessions: March 30, 2025



International Conference on Compilers, Architectures, and Synthesis for Embedded Systems

ACM/IEEE CASES is a premier forum where researchers, developers and practitioners exchange information on the latest advances in compilers and architectures for high-performance, low-power embedded systems. The conference has a long tradition of showcasing leading edge research in embedded processor, memory, interconnect, storage architectures and related compiler techniques targeting performance, power, predictability, security, reliability issues for both traditional and emerging application domains. In addition, we invite innovative papers that address design, synthesis, and optimization challenges in heterogeneous and accelerator-rich architectures.

International Conference on Hardware/Software Codesign and System Synthesis



The ACM/IEEE International Conference on Hardware/Software Codesign and System Synthesis (CODES+ISSS) is the premier event in system-level design, modeling, analysis, and implementation of modern embedded and cyber-physical systems, from system-level specification and optimization down to system synthesis of multi-processor hardware/software implementations. The conference is a forum bringing together academic research and industrial practice for all aspects related to system-level and hardware/software co-design.

Symposia

Tutorials

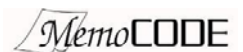


International Conference on Embedded Software

The ACM/IEEE International Conference on Embedded Software (EMSOFT) brings together researchers and developers from academia, industry, and government to advance the science, engineering, and technology of embedded software development. Since 2001, EMSOFT has been the premier venue for cutting-edge research in the design and analysis of software that interacts with physical processes, with a long-standing tradition for results on cyber-physical systems, which compose computation, networking, and physical dynamics.

Workshops

International Symposium on Formal Methods and Models for System Design



MEMOCODE focuses on formal methods and models for developing computer systems and their components. MEMOCODE's objective is to emphasize the importance of models and methodologies in correct system design and development.

Keynotes

Journal-integrated Publication Model: All full papers accepted for the three conferences will be published in the **ACM Transactions on Embedded Computing Systems (TECS)**



Embedded Systems Week

www.esweek.org

September 28—October 3, 2025

Taipei, Taiwan



Paper Process

ESWEEK 2025 has two publication venues: Journal track and Late Breaking (LB) Result track. **(1) Journal track papers**, which are full-length papers describing mature work, will be published in the ACM Transactions on Embedded Computing Systems (TECS). **(2) The LB Result track papers** disseminate complete and mature works written in a condensed form (limited to 4 pages) and will be published in IEEE Embedded Systems Letters (ESL). These two venues (full-length, LB), are mutually exclusive. For more information on the publishing process, please refer to <http://www.esweek.org/author-information>.

Workshops, Tutorials, Special Sessions

Call for Workshop Proposals

ESWEEK 2025 will host several workshops on October 2-3 and is soliciting proposals for new and recurring workshops. Workshops can be half-day to two-day long. We invite you to submit workshop proposals on any topic related to the broad set of research and education.

Call for Tutorial Proposals

ESWEEK 2025 is looking for high-quality tutorials that will take place on Sunday, September 28, 2025. Tutorials on all topics related to embedded system design, analysis, and development are welcome. Tutorials can be either half/full-day, lecture-style or hands-on.

Call for Education Proposals

ESWEEK 2025 will host several education lectures *virtually*, and is soliciting proposals for such lectures. We invite you to submit education proposals on a topic related to this theme.

Call for Special Session Proposals

We invite you to submit special session proposals on any topic relevant to the broad areas of interest of ESWEEK. The special session should cover a topic that is contemporary, hot, and **complementary to the regular sessions**. The special session should be able to generate enthusiasm among the ESWEEK participants.

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