

ESWEEK 2026: Barcelo Sants hotel, Barcelona, Spain

All times are Barcelona Time (CEST; GMT+2)				
Friday, October 2 - Education Classes				
	Virtual	Virtual	Virtual	Virtual
8:00 - 10:00 CEST 23:00 - 1:00 MST 7:00 - 9:00 BST 9:00 - 11:00 EEST 11:30 - 13:30 IST	Education Class 1	Education Class 2	Education Class 3	Education Class 4
13:00 - 15:00 CEST 4:00 - 6:00 MST 12:00 - 14:00 BST 14:00 - 16:00 EEST 16:30 - 18:30 IST	Education Class 5	Education Class 6	Education Class 7	Education Class 8
18:00 - 20:00 CEST 9:00 - 11:00 MST 17:00 - 19:00 BST 19:00 - 21:00 EEST 21:30 - 23:30 IST	Education Class 9	Education Class 10		

Sunday, October 4 - Tutorials				
	ROOM: MR05	ROOM: MR06	ROOM : MR07	ROOM: MR08
09:00 - 10:30	<i>Tutorial 1</i> Accelerating Alpamayo 1 Inference via Architectural and System-Level Optimizations	<i>Tutorial 2</i> From Application Development to Design-Space Exploration with CEDR for Heterogeneous Systems	<i>Tutorial 3</i> Hardware-Aware Compilation for Analog, Digital, and Heterogeneous Computing-in-Memory Systems	Qualcomm Arduino Hackathon
10:30 - 11:00	Coffee Break (North Shuttle)			
11:00 - 12:30	<i>Tutorial 1</i> Accelerating Alpamayo 1 Inference via Architectural and System-Level Optimizations	<i>Tutorial 2</i> From Application Development to Design-Space Exploration with CEDR for Heterogeneous Systems	<i>Tutorial 3</i> Hardware-Aware Compilation for Analog, Digital, and Heterogeneous Computing-in-Memory Systems	Qualcomm Arduino Hackathon
12:30 - 13:30	Lunch Break (North Shuttle)			

13:30 - 15:00	<i>Tutorial 4</i> A Pragmatic Guide to Building Conservative Discrete Abstractions of Cyber-Physical Systems	<i>Tutorial 5</i> Large-scale simulation of Quantum circuits using Tensor Networks	<i>Tutorial 6</i> Octopus — An Open Framework for Prototyping and Validating Memory Solutions in Multicore Real-Time Systems	Qualcomm Arduino Hackathon
15:00 - 15:30	Coffee Break (North Shuttle)			
15:30 - 17:00	<i>Tutorial 4</i> A Pragmatic Guide to Building Conservative Discrete Abstractions of Cyber-Physical Systems	<i>Tutorial 5</i> Large-scale simulation of Quantum circuits using Tensor Networks	<i>Tutorial 6</i> Octopus — An Open Framework for Prototyping and Validating Memory Solutions in Multicore Real-Time Systems	Qualcomm Arduino Hackathon
18:00 - 19:30	Welcome Reception (North Shuttle)			

Monday, October 5 - Main Conferences, Day 1					
ROOM: MR10+11					
08:30 - 09:00	Opening Session				
09:00 - 10:00	KEYNOTE 1: "Agentic Design Automation for Embedded Systems: The Promise, the Peril, and the Proofs", Prof. Farinaz Koushanfar, UC San Diego				
10:00 - 10:30	Coffee Break (North Shuttle + Orbital Shuttle)				
	ROOM: MR05+06	ROOM: MR07+08	ROOM: MR12+13	ROOM: MR14	ROOM: MR10+11 (front rows)
10:30 - 12:00	Session 1, CODES Speeding up LLMs with Sparsity, Quantization, and Speculation	Session 1, CASES Hardware Synthesis and Design Automation	Session 1, EMSOFT Edge AI and Autonomous Driving	Session 2, CODES Emerging Threats and Defenses in Hardware Security	
12:00 - 12:30	Poster Session (North Shuttle)				
12:30 - 13:30	Lunch Break (North Shuttle + Orbital Shuttle)				
13:30 - 15:00	Session 3, CODES Emerging Accelerator Architectures and Hardware-Algorithm Co-Design	Session 2, CASES Efficient AI Processing and Acceleration	Session 2, EMSOFT Formal Verification of Cyber-Physical and Hybrid Systems	Session 3, CASES Adaptive Computing and System Optimization	Embedded System Software Competition (ESSC)
15:00 - 15:30	Coffee Break & Poster Session (North Shuttle + Orbital Shuttle)				
15:30 - 17:00	Session 4, CODES Compute-in-Memory for the Edge	Session 4, CASES Compilation, Optimization, and Verification	Session 3, EMSOFT Embedded Systems Performance, Energy, and Resource Management	Session 4, EMSOFT Verified Software, Model Extraction, and Formal Code Generation	Embedded System Software Competition (ESSC)
17:00 - 17:30	Poster Session (North Shuttle)				

Tuesday, October 6 - Main Conferences, Day 2	
ROOM: MR10+11	
08:30 - 09:00	Award Ceremony (Test of Time Awards and Society Awards)
09:00 - 10:00	KEYNOTE 2: "Distributed Time-Sensitive Systems", Prof. Edward Lee, UC Berkeley
10:00 - 10:30	Coffee Break (North Shuttle + Orbital Shuttle)

	ROOM: MR05+06	ROOM: MR07+08	ROOM: MR12+13	ROOM: MR14	ROOM: MR10+11 (front rows)
10:30 - 12:00	Session 5, CODES Scalable and Resilient Storage Systems	Session 5, CASES Efficient LLM Systems and Architectures	Session 5, EMSOFT Real-Time Scheduling, Timing, and Control	Session 6, CODES Design and Optimization of Heterogeneous Computing Systems	ACM SIGBED Student Research Competition (SRC)
12:00 - 12:30	Poster Session (North Shuttle)				
12:30 - 13:15	Lunch Break (North Shuttle + Orbital Shuttle)				
	ROOM: MR10+11				
13:15 - 14:00	IEEE CEDA LUNCHEON KEYNOTE, "The Age of AI – the Good, the Bad, and the Ugly", Dr. Hsien-Hsin Sean Lee, Intel				
	ROOM: MR05+06	ROOM: MR07+08	ROOM: MR12+13	ROOM: MR14	ROOM: MR10+11 (front rows)
14:00 - 15:30	Session 7, CODES Design Automation and Hardware Design with AIs	Session 6, CASES Data-Centric and Near-Memory Architectures	Session 6, EMSOFT Efficient AI Scheduling, Acceleration, and Hardware/Algorithm Co-Design	Session 7, EMSOFT Security, Resilience, and Robustness of CPS	
15:30 - 16:00	Coffee Break & Poster Session (North Shuttle + Orbital Shuttle)				
	A Special Afternoon of Discovery: Special Sessions				
16:00 - 17:30	Special Session 1, CODES Chiplets at Scale: Overcoming Design, Thermal, Packaging, and Security Challenges for Next-Generation Heterogeneous Systems	Special Session 2, CASES A Cross-Stack Approach to Efficient and Scalable Generative AI	Special Session 3, EMSOFT Making Time-Sensitive Networking Deployable: A Comprehensive Lifecycle Architecture	Special Session 4, CODES Neuromorphic and Analog Computing for AI-Driven Autonomous Laboratories and Robotics	Special Session 5, CASES Hardware-Software Co-Design of Quantum Machine Learning Systems
19:30 - 23:00	Banquet – Drassanes Reials de Barcelona (Royal Shipyards of Barcelona)				

Wednesday, October 7 - Main Conferences, Day 3					
	ROOM: MR10+11				
08:30 - 09:00	Award Ceremony (Best Paper Awards, Competition Awards, and Society Awards)				
09:00 - 10:00	KEYNOTE 3: TITLE TBA, Speaker: Dr. Orlando Moreira, Snap Inc.				
10:00 - 10:30	Coffee Break (North Shuttle + Orbital Shuttle)				
	ROOM: MR05+06	ROOM: MR07+08	ROOM: MR12+13	ROOM: MR14	ROOM: MR10+11 (front rows)
10:30 - 12:00	Session 8, CODES Efficient and Adaptive Edge Sensing Systems	Session 7, CASES Security, Cryptography, and Resilience	Session 8, EMSOFT Distributed, Networked, and Coordinated Cyber-Physical Systems	Session 9, EMSOFT Embedded Software, Storage, OS, and Verified Low-Level Implementation	PhD Forum 1
12:00 - 12:30	Poster Session (North Shuttle)				
12:30 - 13:30	Lunch Break (North Shuttle + Orbital Shuttle)				
13:30 - 15:00	Session 9, CODES Efficient and Robust AI Systems and Architectures	Session 8, CASES Emerging and Specialized Architectures	Session 10, EMSOFT Timing Semantics, Runtime Monitoring, and Specification Analysis	Session 9, CASES Storage and Persistent-Memory Systems	PhD Forum 2
15:00 - 15:30	Coffee Break & Poster Session (North Shuttle + Orbital Shuttle)				
	ROOM: MR10+11				

15:30 - 17:00	PANEL: "Beyond the Prompt: Embedded Education and Research in the Age of AI"
17:00 - 17:30	Closing Session

Thursday, October 8 - Symposium and Workshops, Day 1					
	ROOM: MR01	ROOM: MR02	ROOM: MR03	ROOM: MR4.1	ROOM: MR4.2
09:00 - 10:30	SDS	NIPS	RSP	MEMOCODE	TCRS
10:30 - 11:00	Coffee Break				
11:00 - 12:30	SDS	NIPS	RSP	MEMOCODE	TCRS
12:30 - 13:30	Lunch Break				
13:30 - 15:00		NIPS	RSP	MEMOCODE	TCRS
15:00 - 15:30	Coffee Break				
15:30 - 17:00		NIPS	RSP	MEMOCODE	TCRS

Friday, October 9 - Symposium and Workshops, Day 2					
	ROOM: MR01	ROOM: MR02	ROOM: MR03	ROOM: MR4.1	ROOM: MR4.2
09:00 - 10:30	WEEPQC	CAIESA	RSP	MEMOCODE	TCRS
10:30 - 11:00	Coffee Break				
11:00 - 12:30	WEEPQC	CAIESA	RSP	MEMOCODE	TCRS
12:30 - 13:30	Lunch Break				
13:30 - 15:00	WEEPQC	CAIESA		MEMOCODE	
15:00 - 15:30	Coffee Break				
15:30 - 17:00	WEEPQC	CAIESA			